

<b>Notice of References Cited</b>	Application/Control No. 10/633,057	Applicant(s)/Patent Under Reexamination TAYLOR, RICHARD F.	
	Examiner Monica Lewis	Art Unit 2822	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2002/0017678	02-2002	Gonzalez et al.	257/314
	B	US-6,475,873	11-2002	Kalnitsky et al.	438/384
	C	US-			
	D	US-			
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	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf et al., Silicon Processing, 2000, Lattice Press, Volume 1, Pages 821, 822 and 834-837.
	V	Peter Van Zant, Microchip Fabrication, 2000, McGraw-Hill, Pages 514 and 515.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.